

FIG. 1A

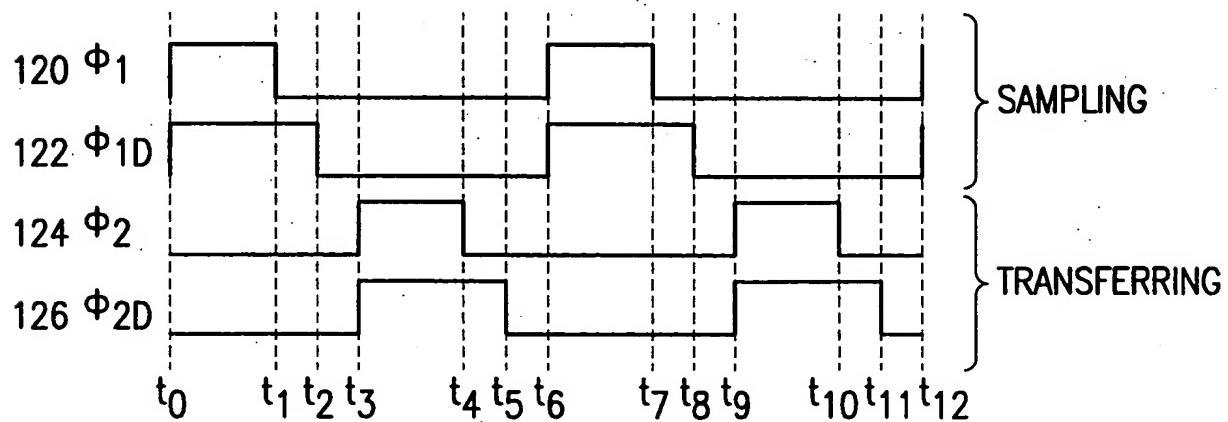
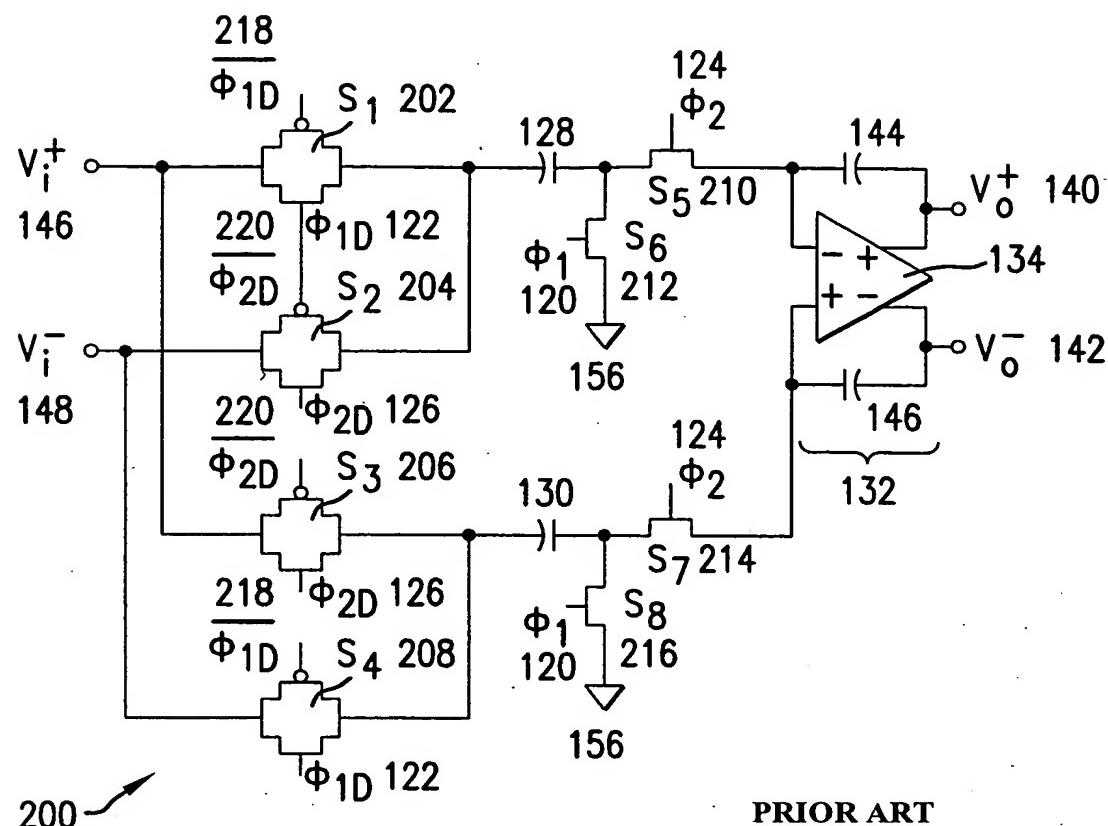
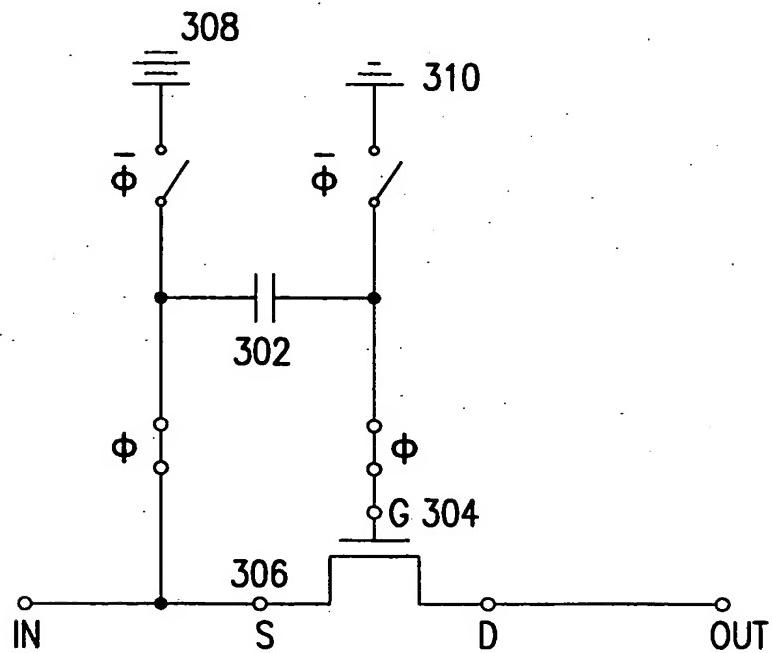


FIG. 1B



PRIOR ART

FIG. 2



300 ↗

FIG. 3

PRIOR ART

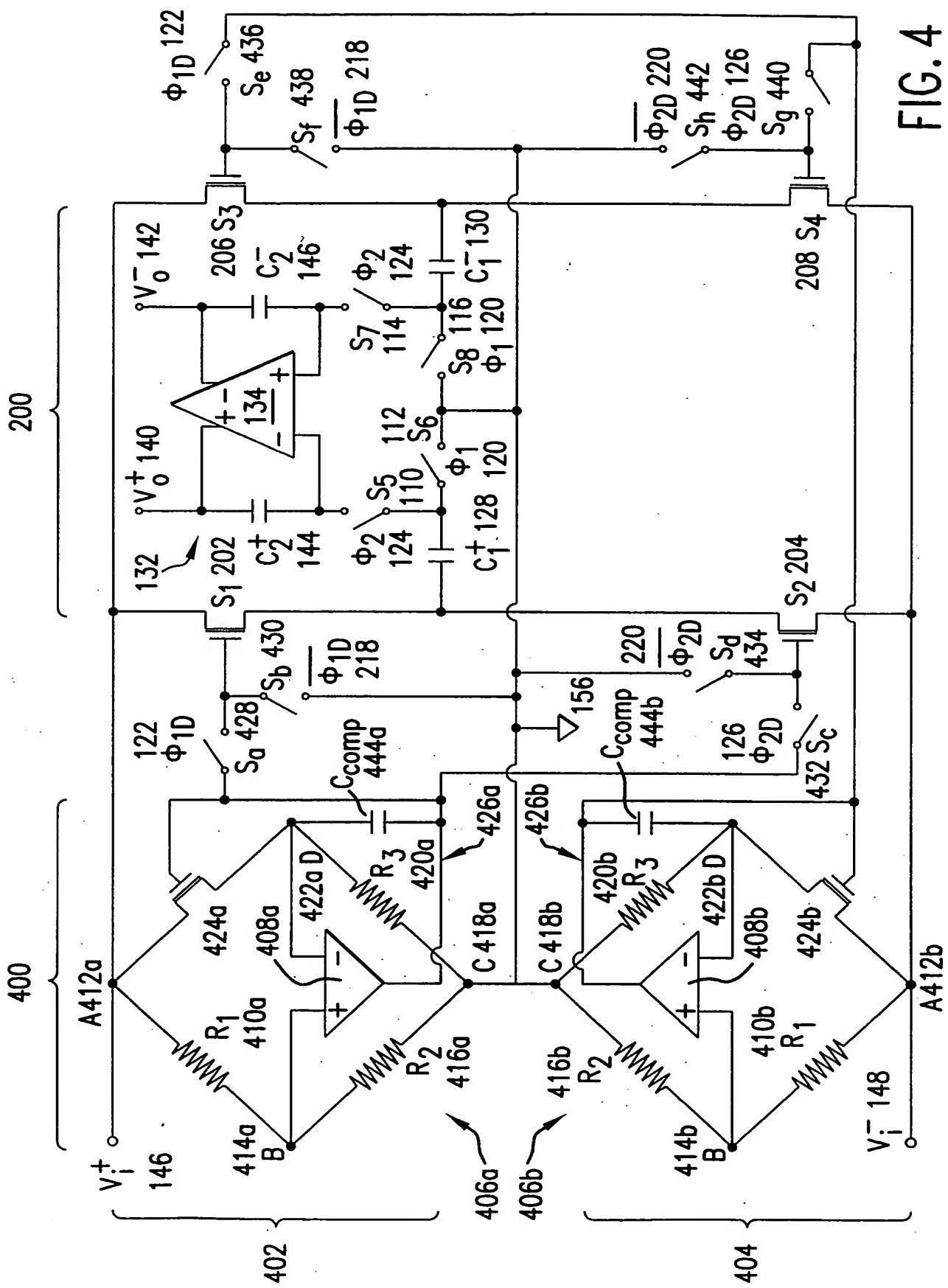


FIG. 4

500

502 CONNECT A VOLTAGE INPUT SIGNAL TO A FIRST NODE OF A BRIDGE CIRCUIT AND TO THE SWITCHED CAPACITOR CIRCUIT

504 REGULATE A GATE VOLTAGE OF A MOSFET RESISTOR DISPOSED IN A RESISTOR BRANCH OF THE BRIDGE CIRCUIT TO CONTROL THE RESISTANCE OF THE MOSFET RESISTOR

506 CONNECT THE REGULATED GATE VOLTAGE TO A GATE TERMINAL OF A SIGNAL CONDUCTING MOSFET SWITCH IN THE SWITCHED CAPACITOR CIRCUIT, THEREBY CONTROLLING THE RESISTANCE OF THE SIGNAL CONDUCTING MOSFET SWITCH SO THAT IT IS INDEPENDENT OF THE VOLTAGE INPUT SIGNAL, THEREBY REDUCING TRACK MODE DISTORTION IN THE SWITCHED CAPACITOR CIRCUIT

FIG. 5

600

602 CONNECT A NONINVERTING TERMINAL OF AN OPERATIONAL AMPLIFIER TO A SECOND NODE OF THE BRIDGE CIRCUIT AND AN INVERTING TERMINAL OF THE OPERATIONAL AMPLIFIER TO A THIRD NODE OF THE BRIDGE CIRCUIT

604 CONNECT AN OUTPUT OF THE OPERATIONAL AMPLIFIER TO A GATE TERMINAL OF A MOSFET RESISTOR DISPOSED IN A RESISTOR BRANCH OF THE BRIDGE CIRCUIT, THEREBY REGULATING THE GATE VOLTAGE OF THE MOSFET RESISTOR TO CONTROL THE RESISTANCE OF THE MOSFET RESISTOR

FIG. 6